

FIG. 1

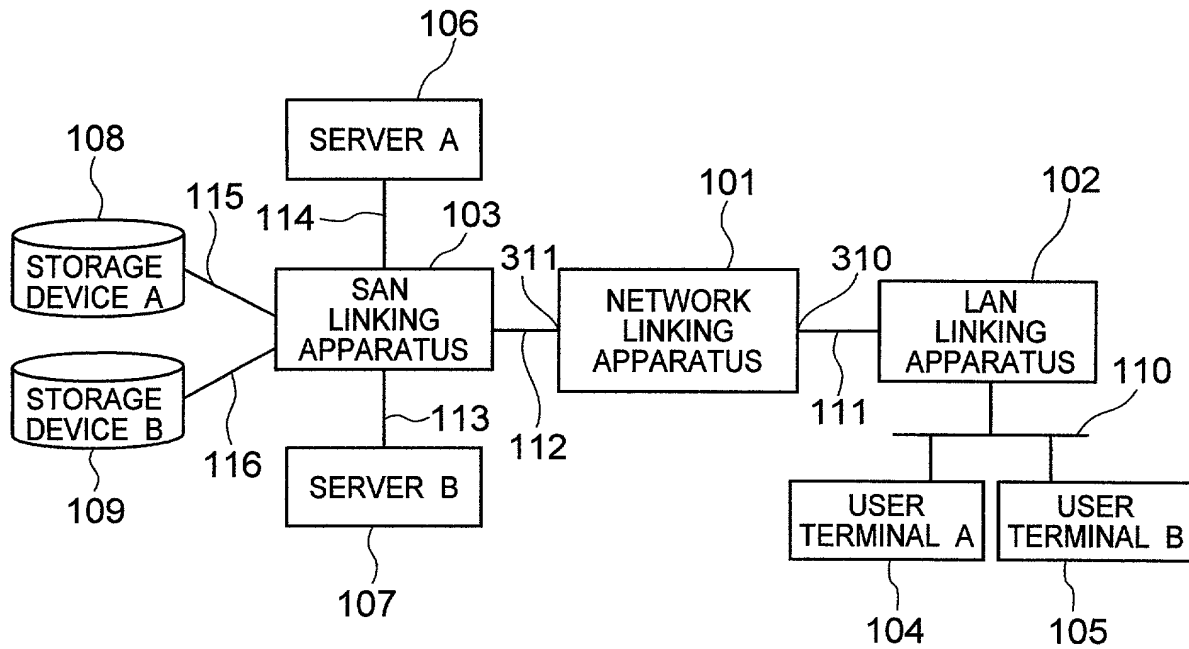


FIG. 2

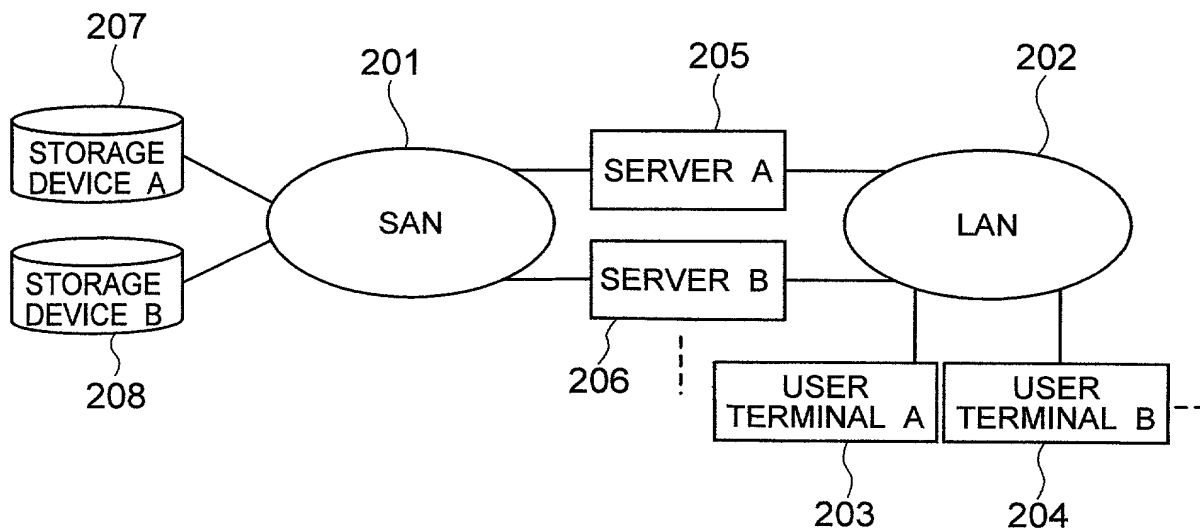


FIG. 3

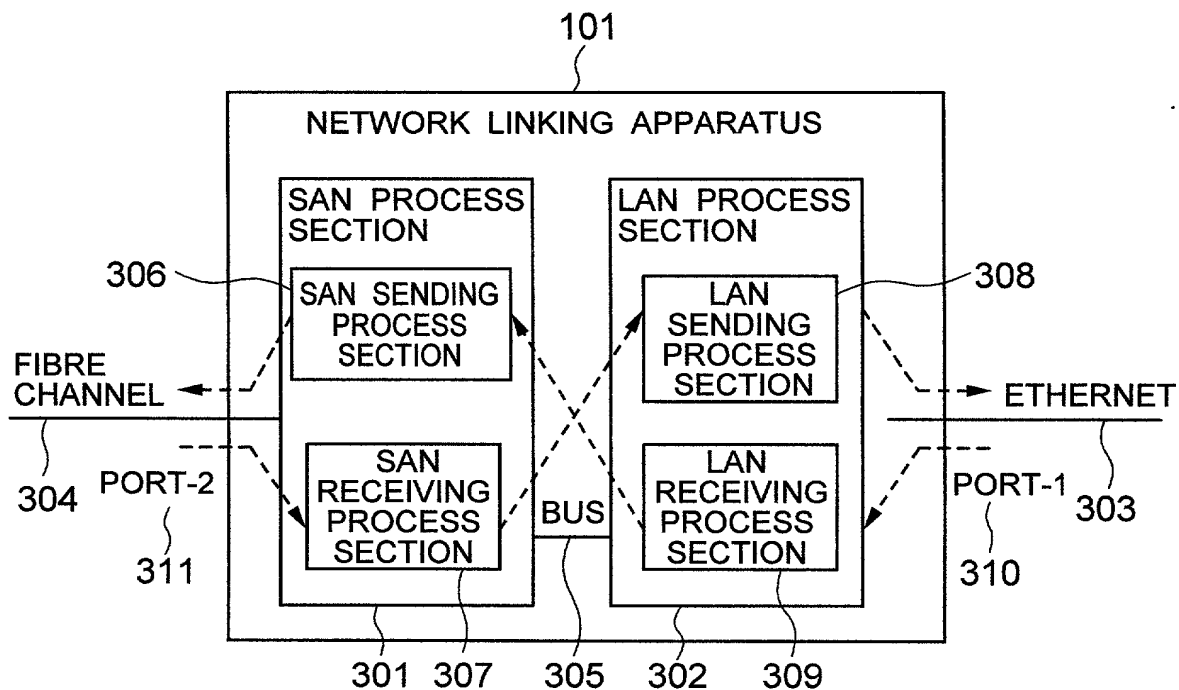


FIG. 4

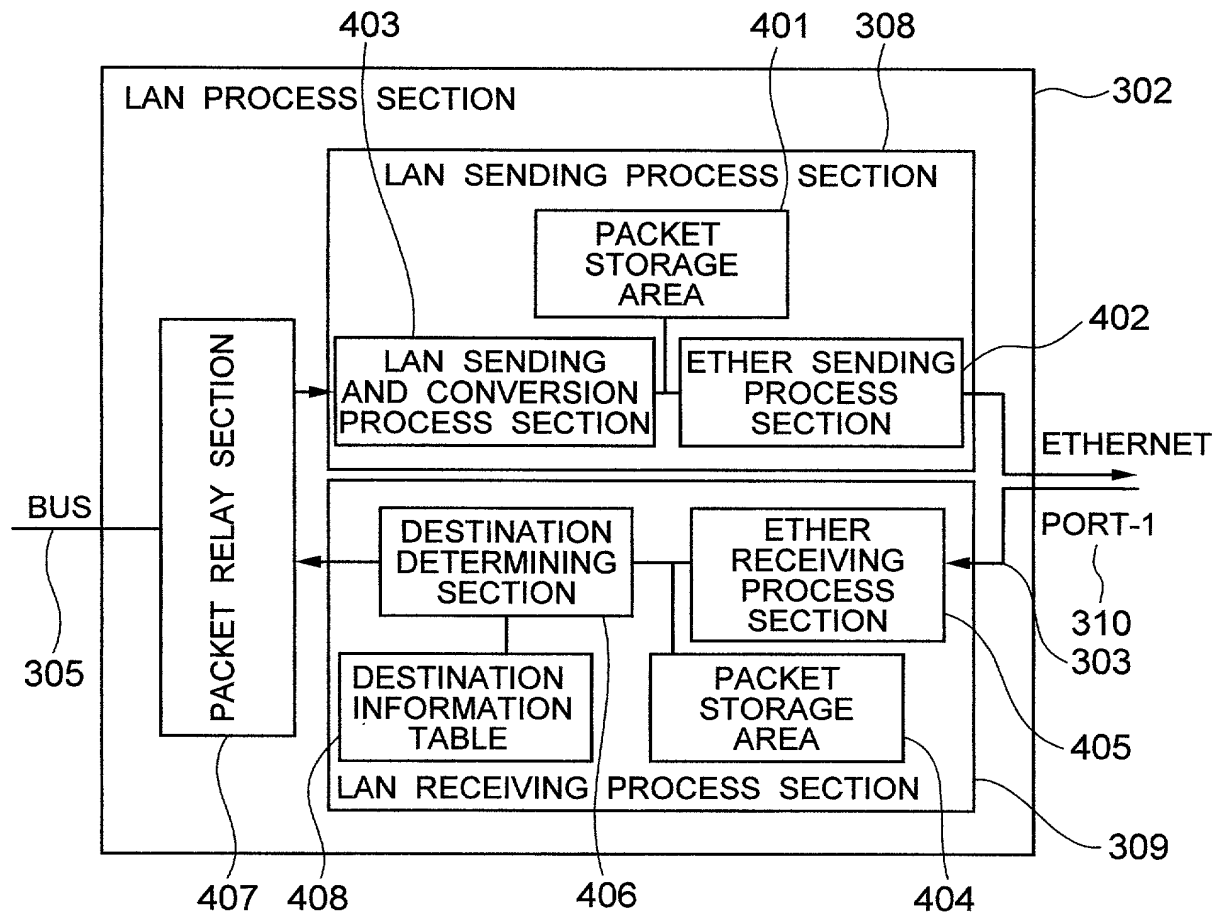


FIG. 5

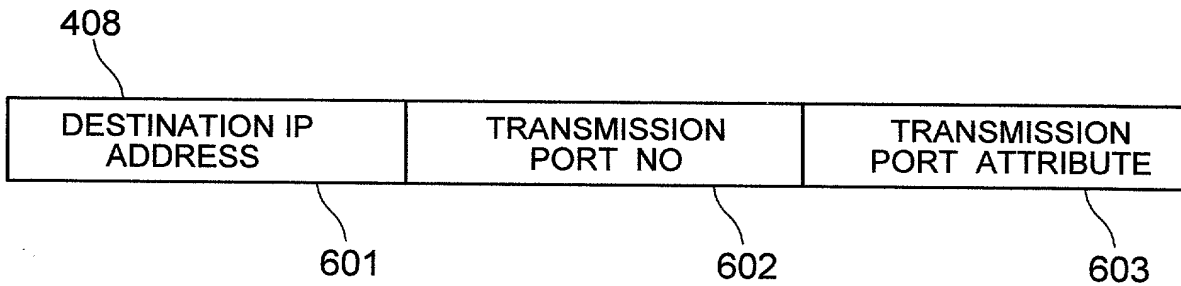


FIG. 6

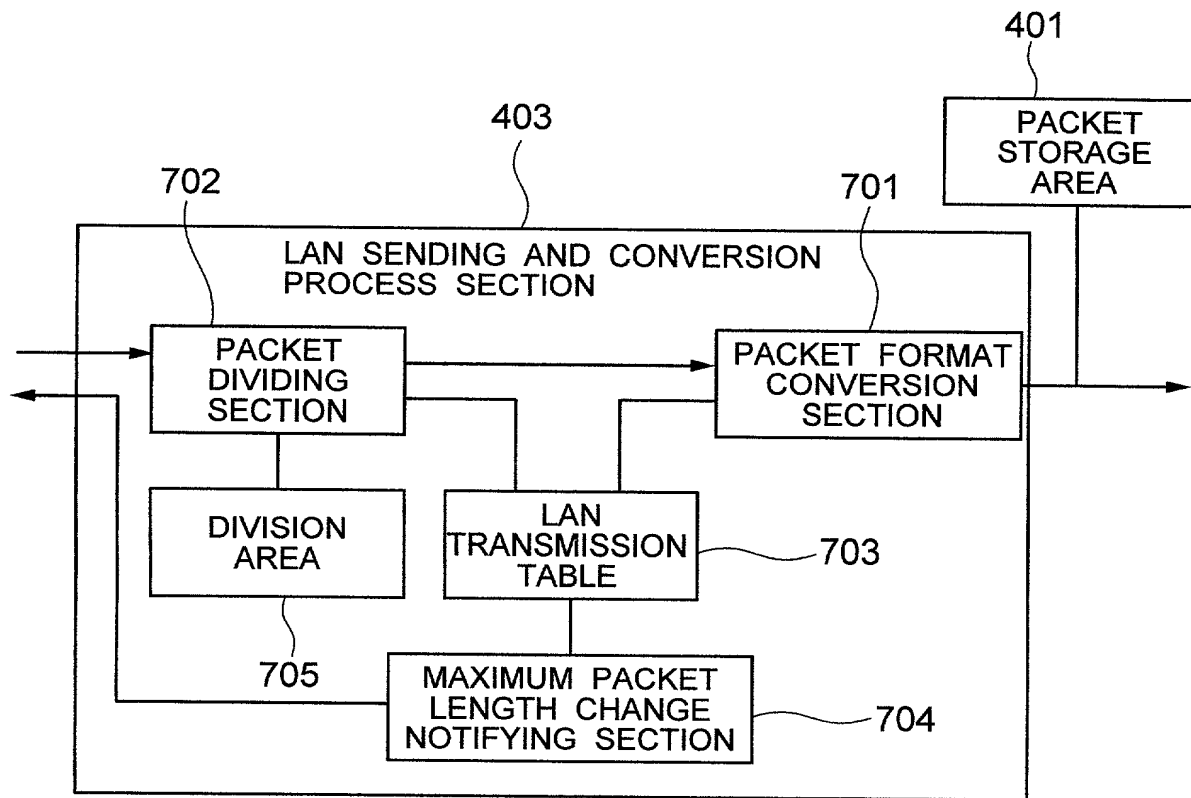


FIG. 7

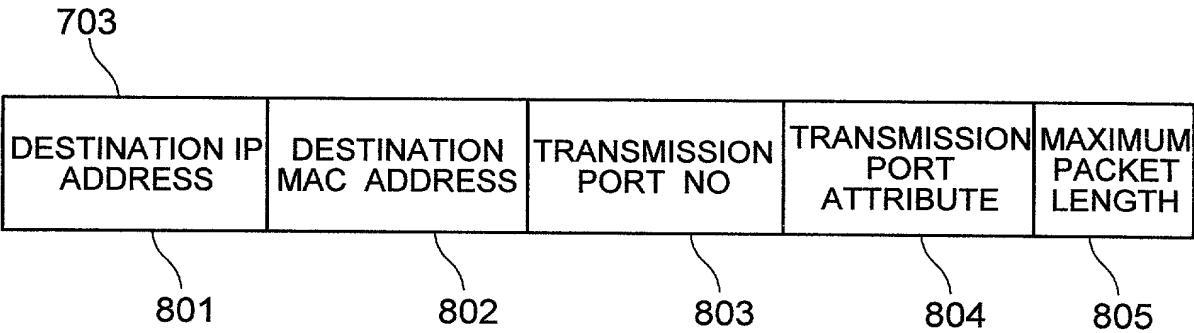


FIG. 8

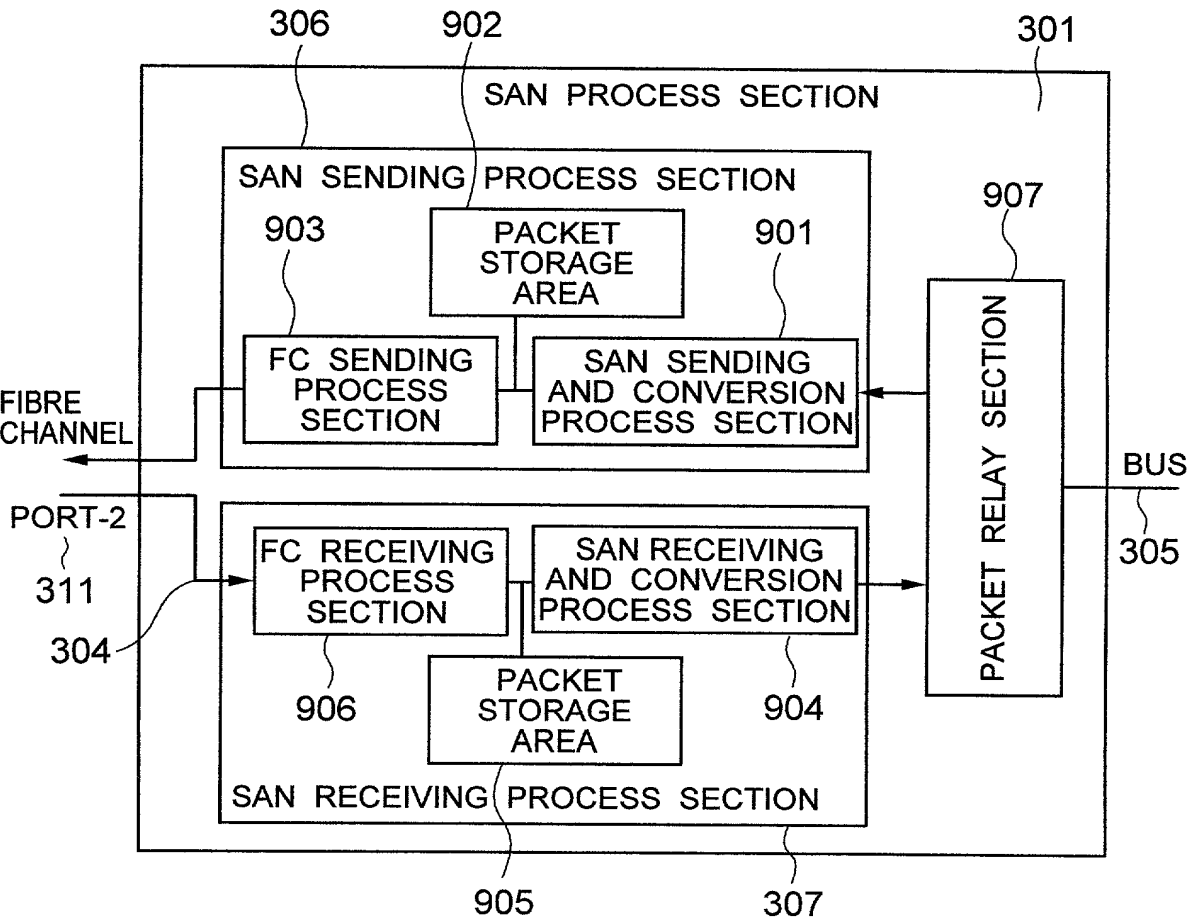


FIG. 9

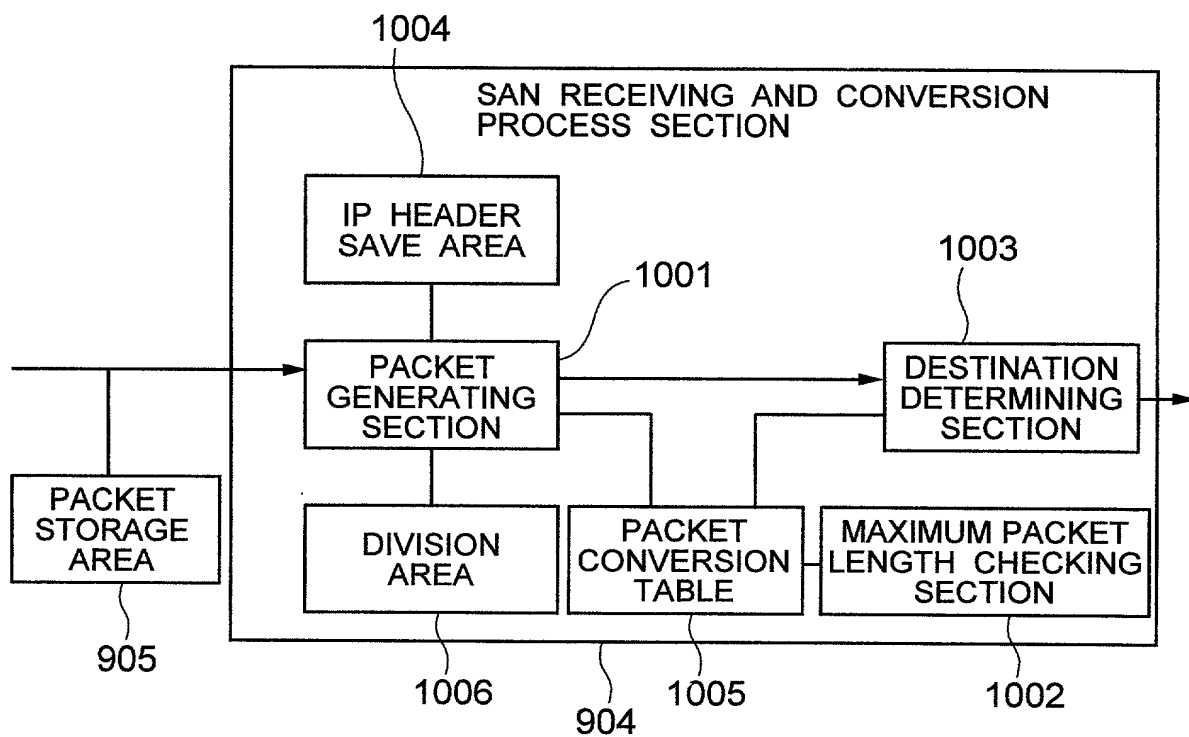


FIG. 10

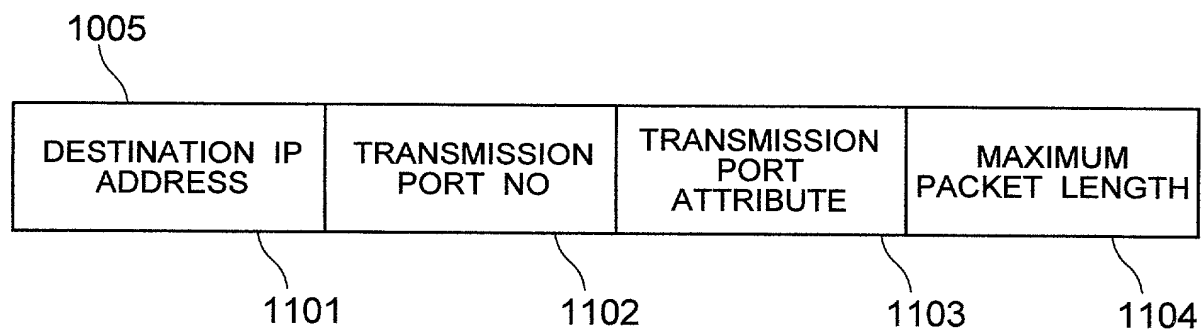


FIG. 11

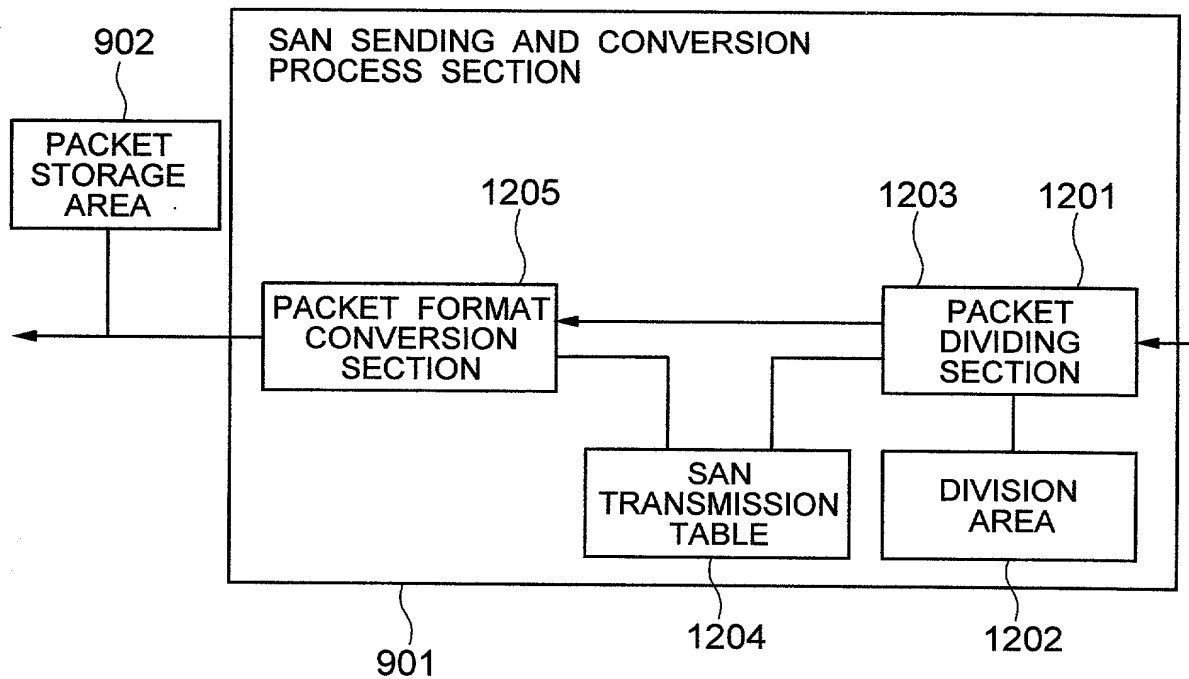


FIG. 12

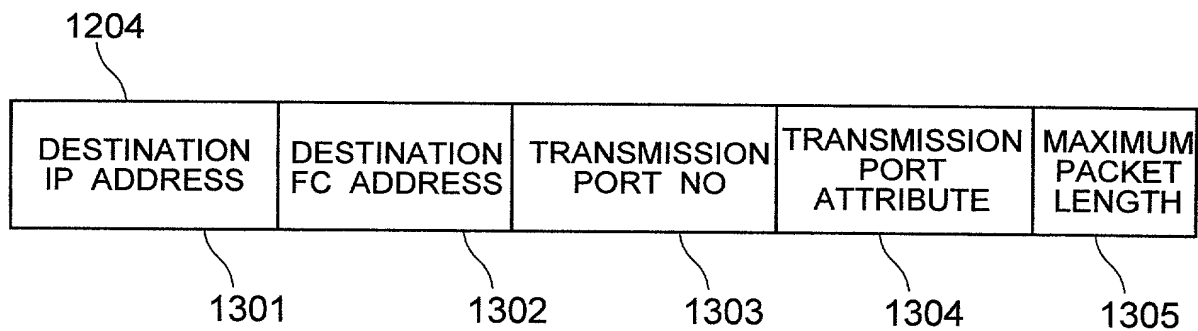


FIG. 13

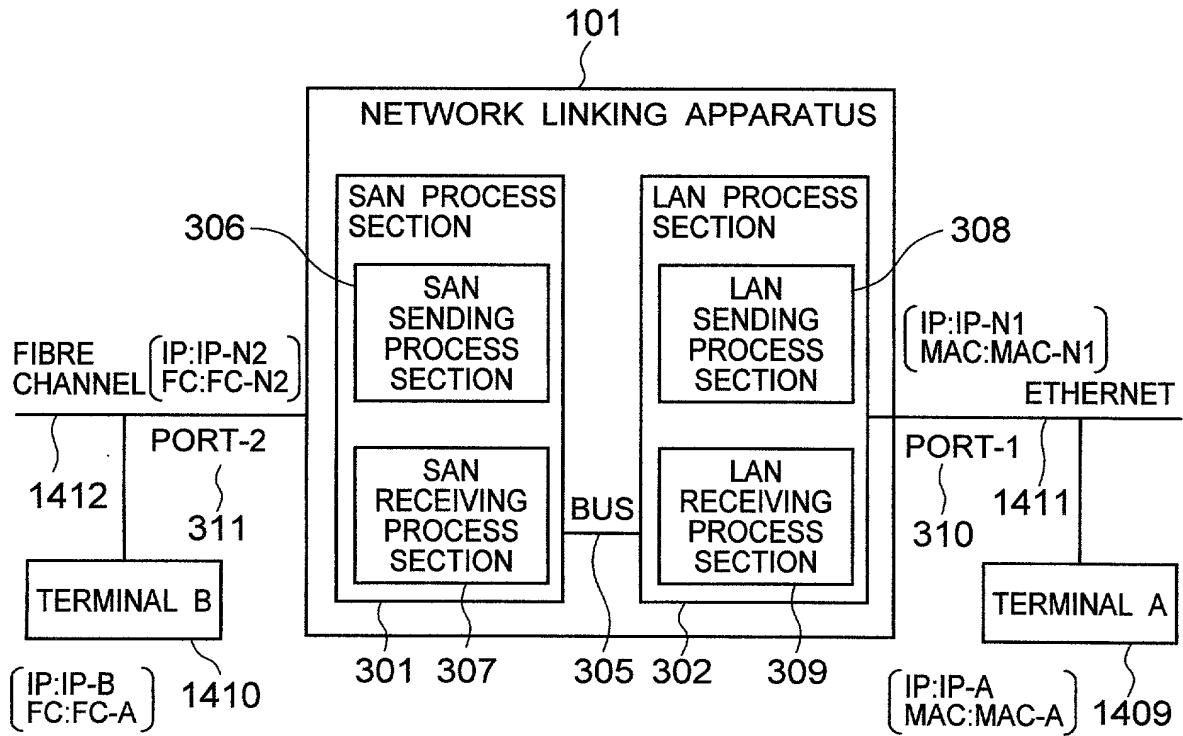


FIG. 14

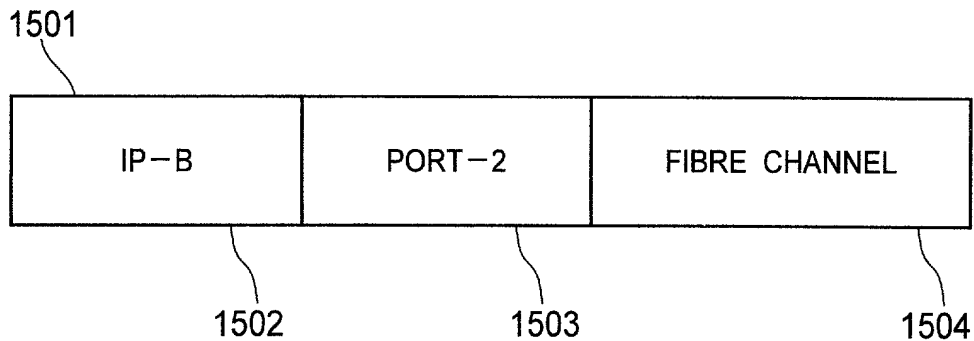


FIG. 15

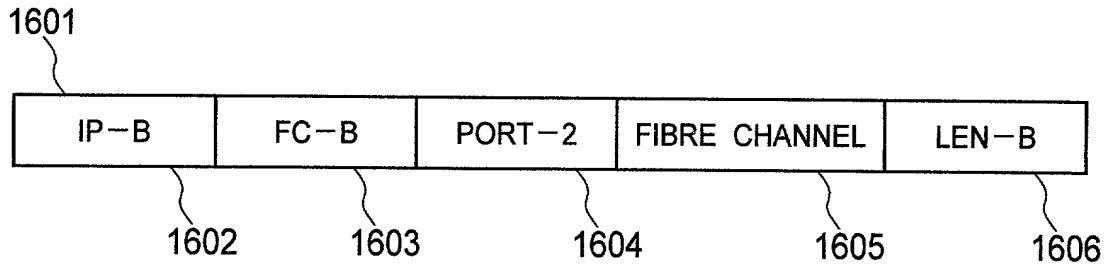


FIG. 16

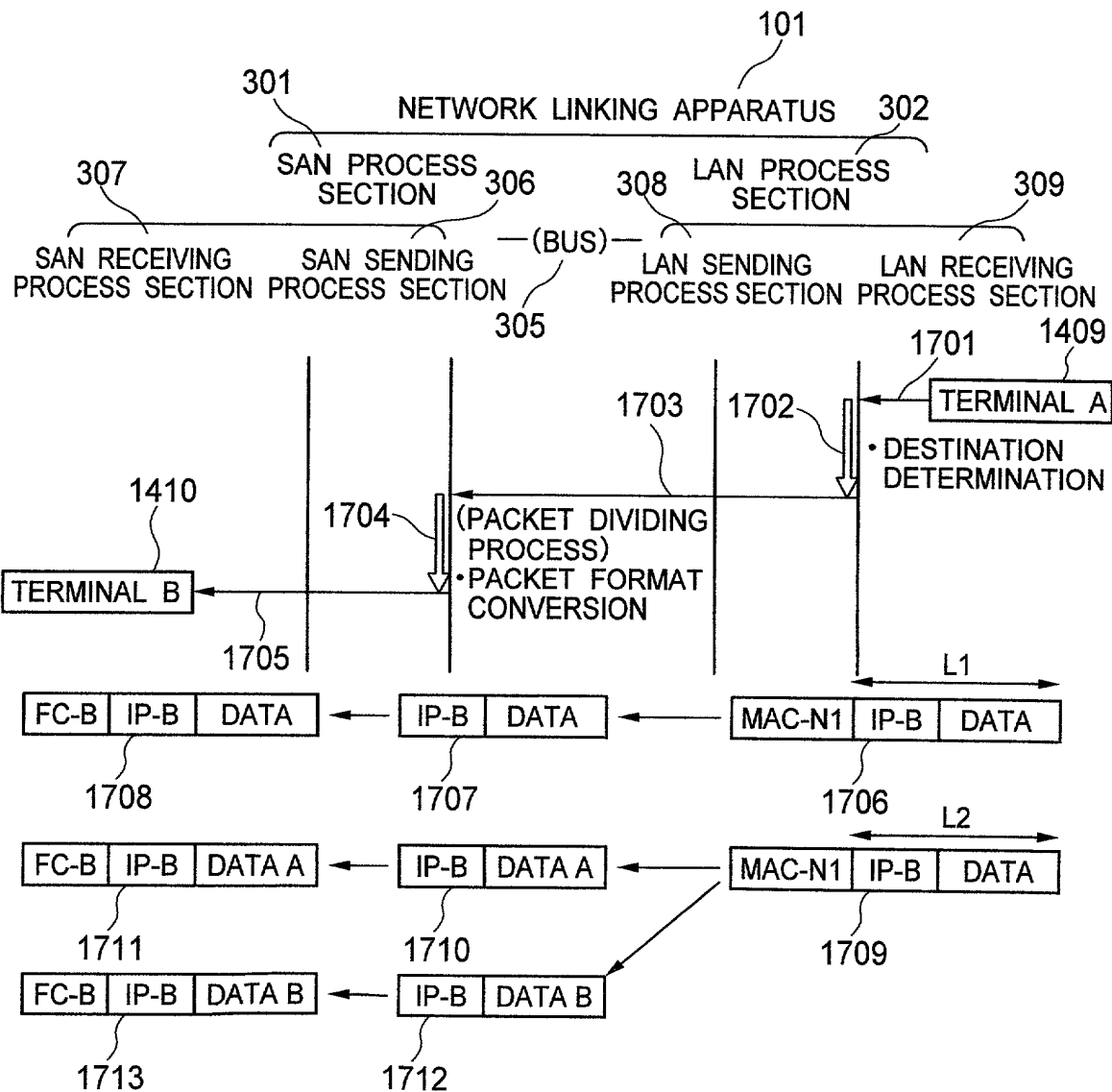


FIG. 17

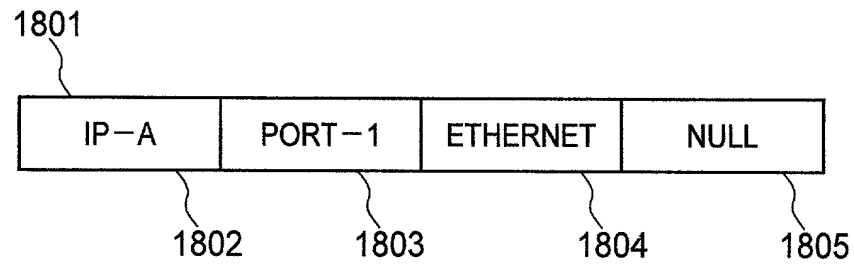


FIG. 18

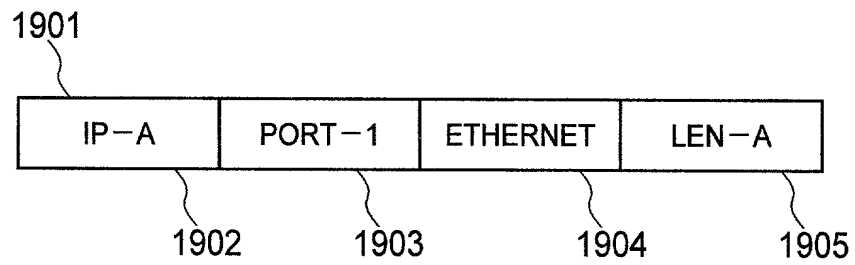


FIG. 19

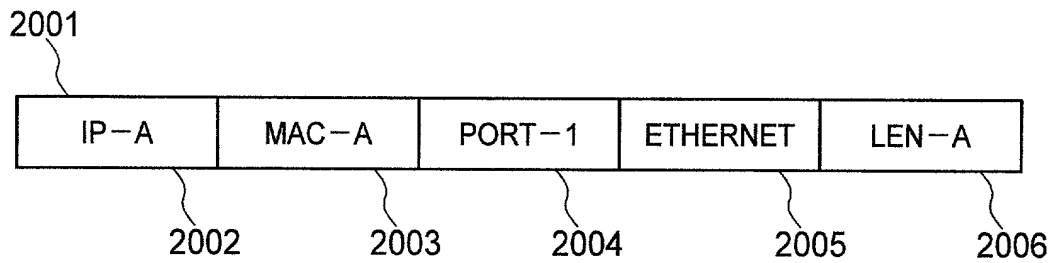


FIG. 20

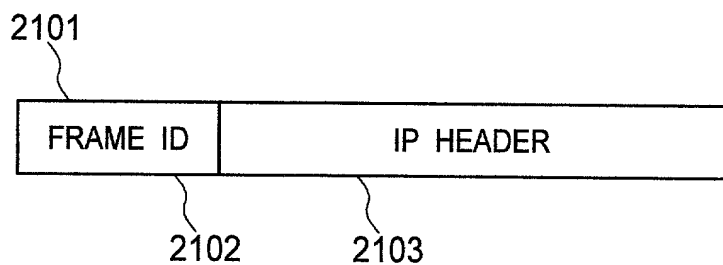


FIG. 21

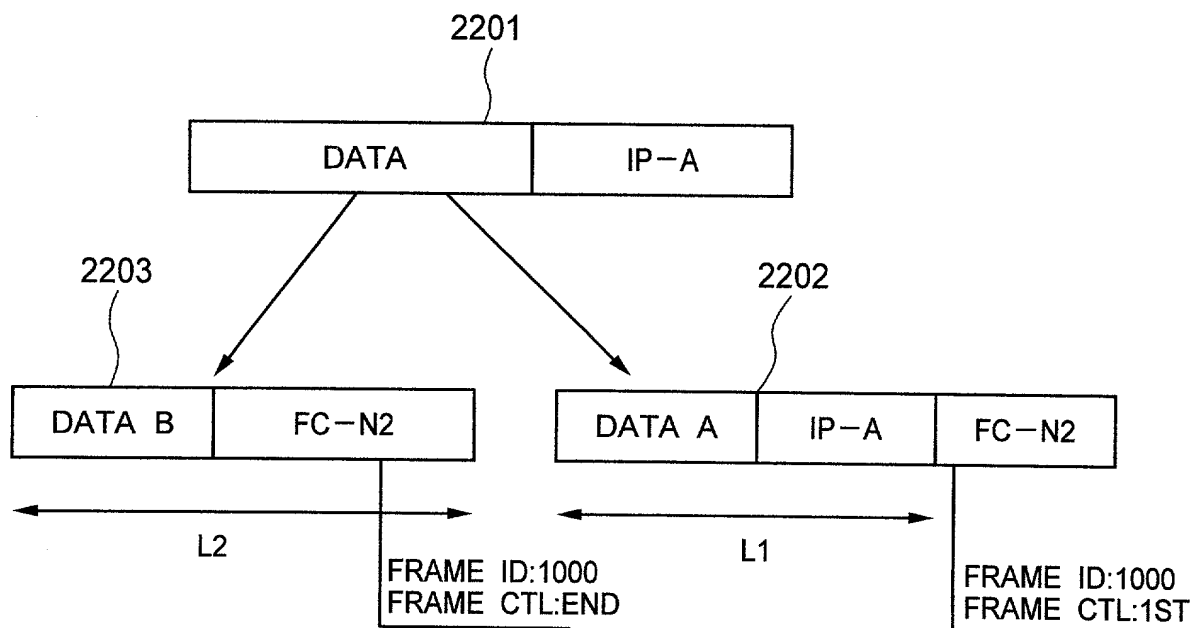


FIG. 22

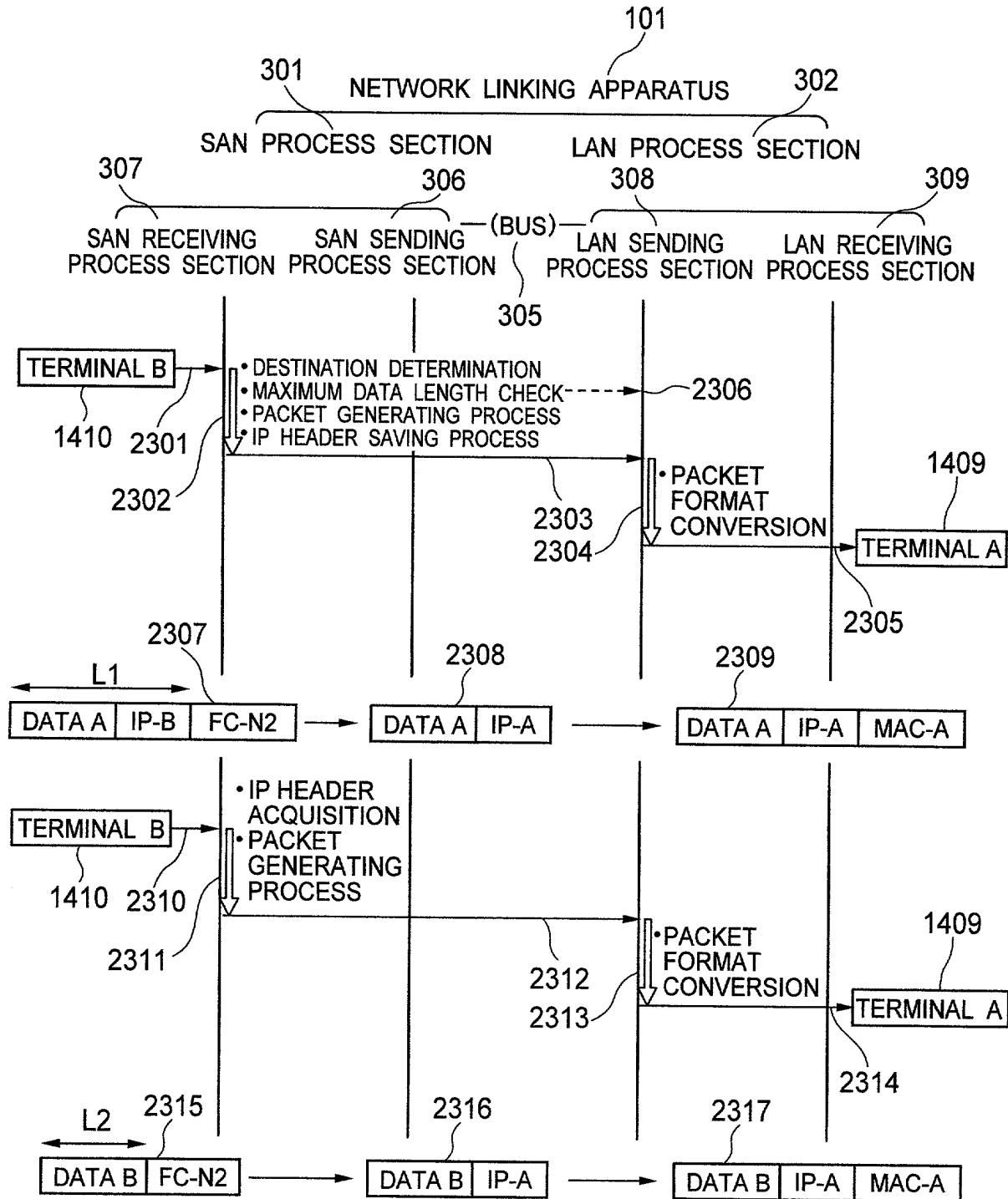


FIG. 23

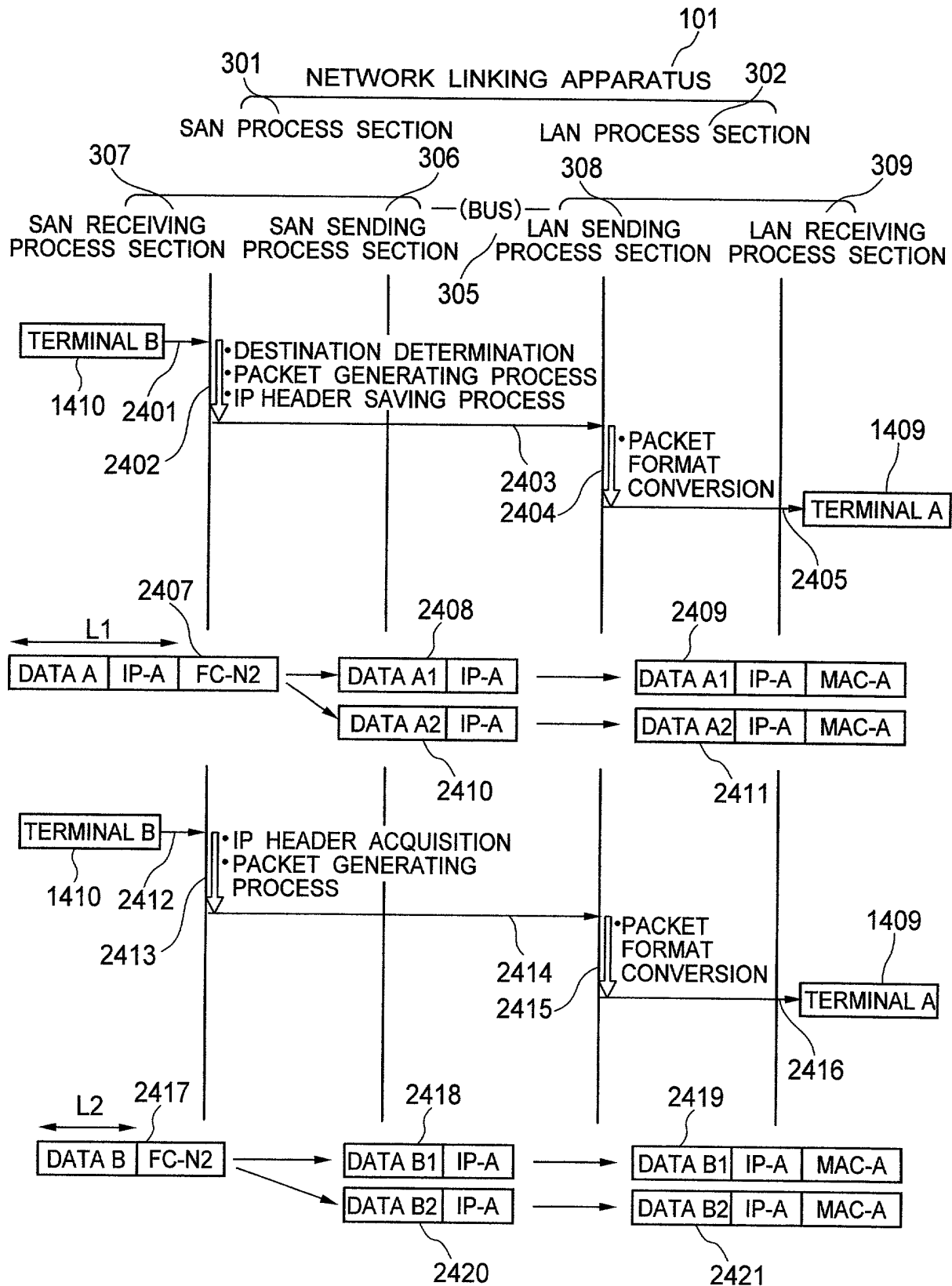


FIG. 24

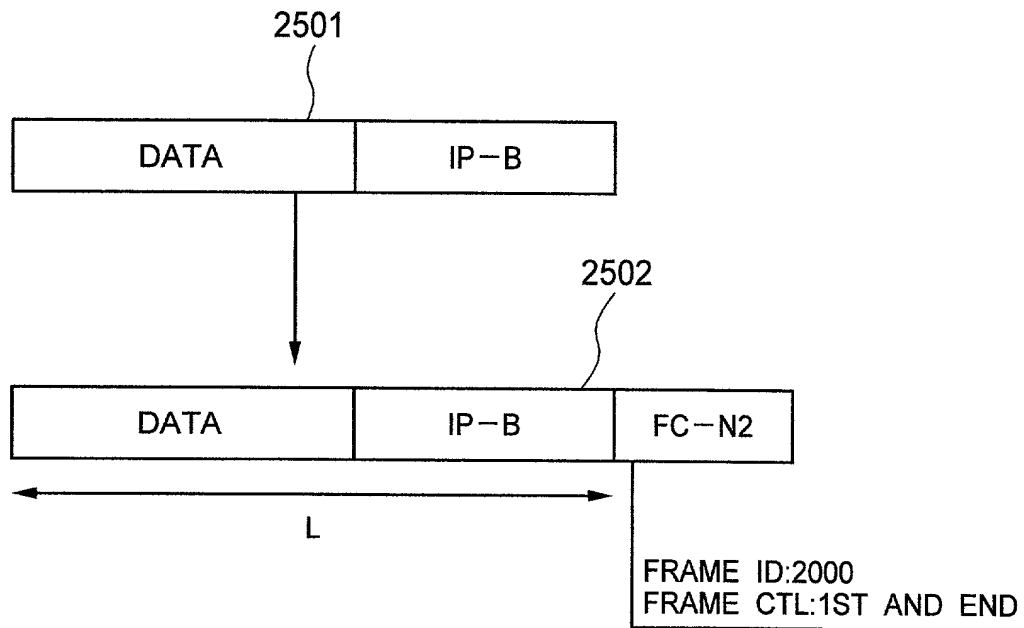


FIG. 25

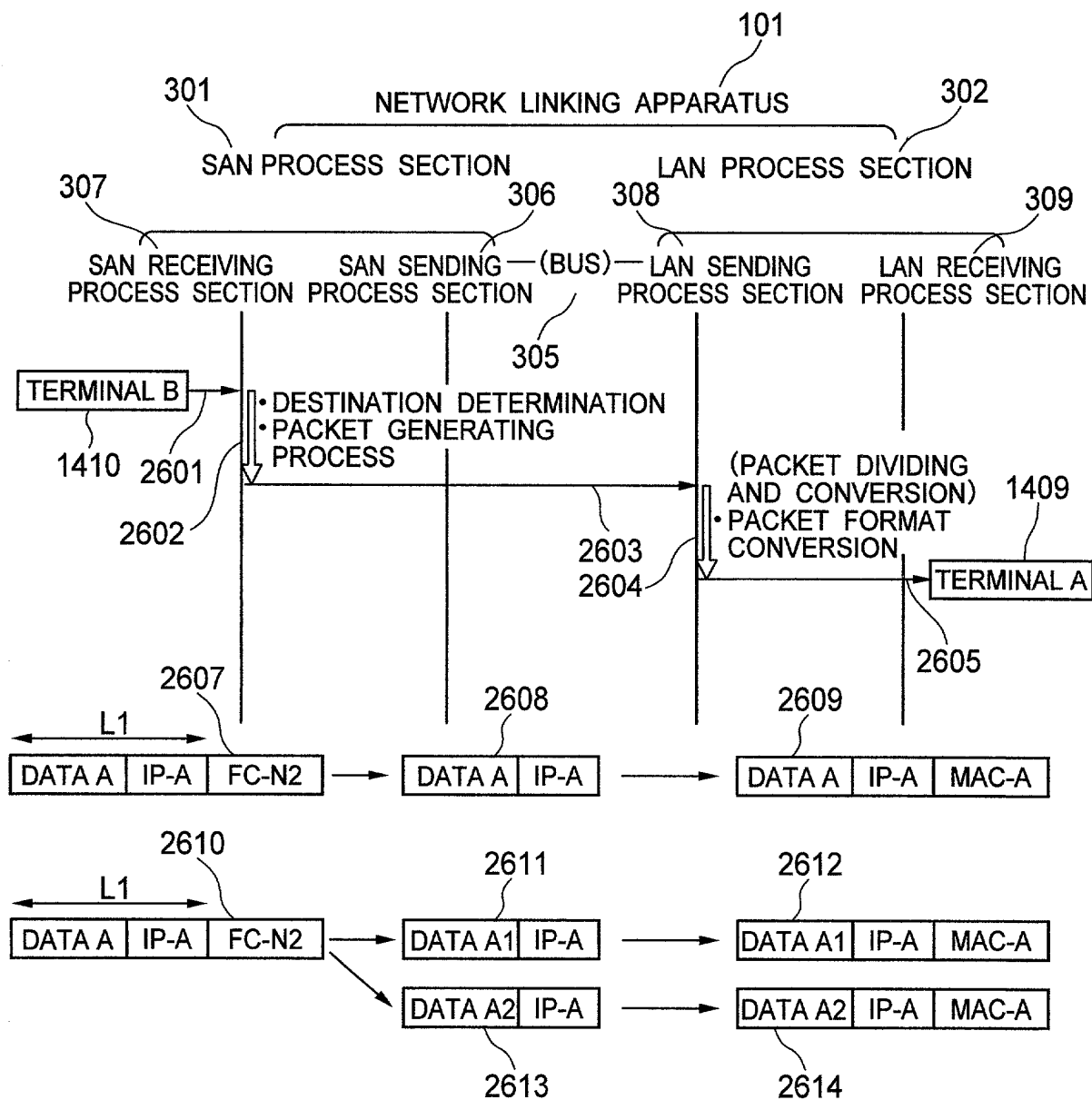


FIG. 26

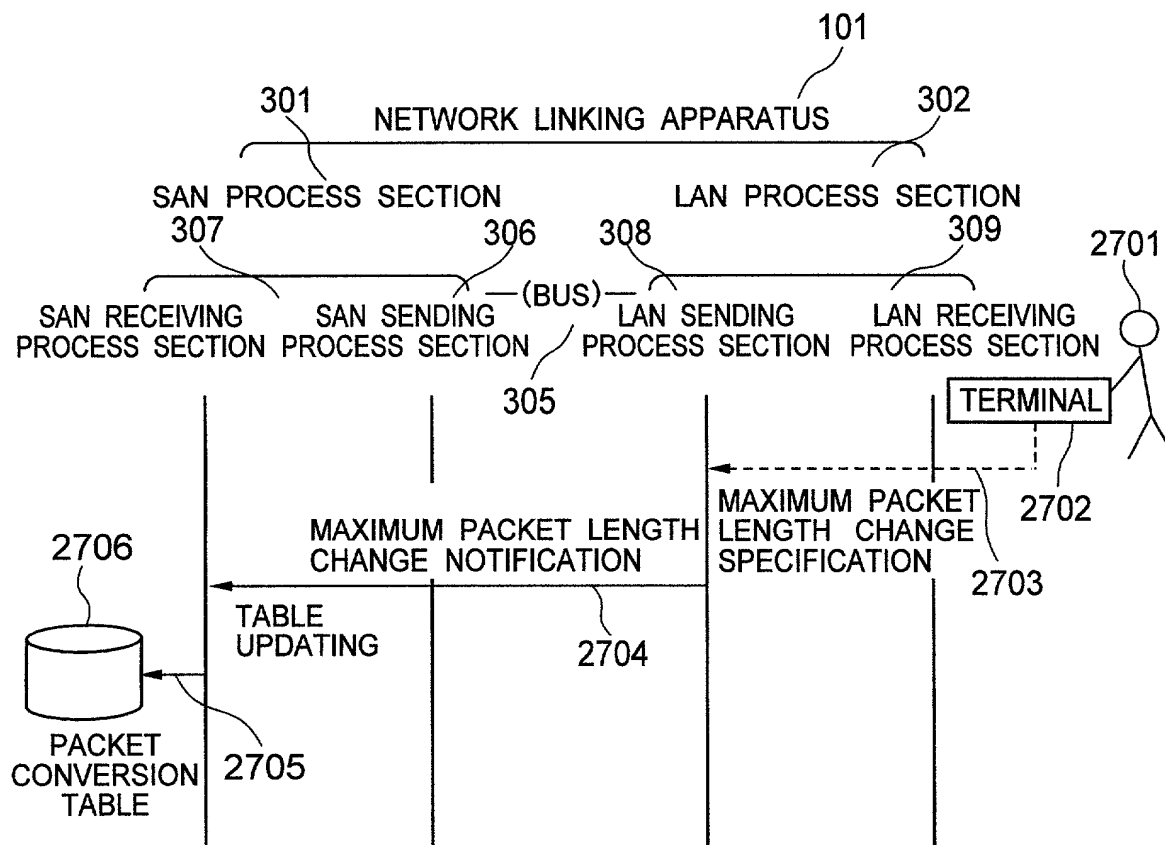


FIG. 27

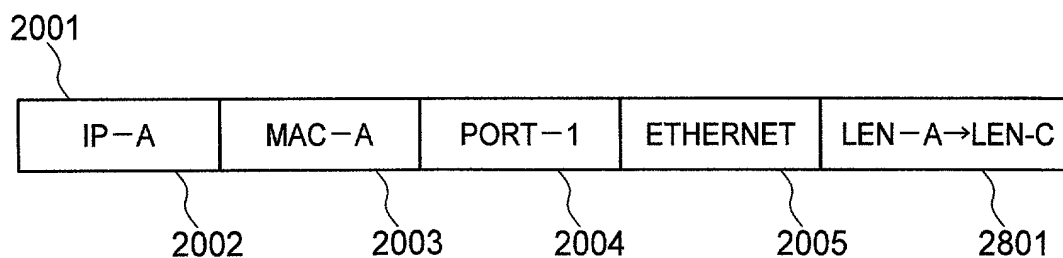


FIG. 28

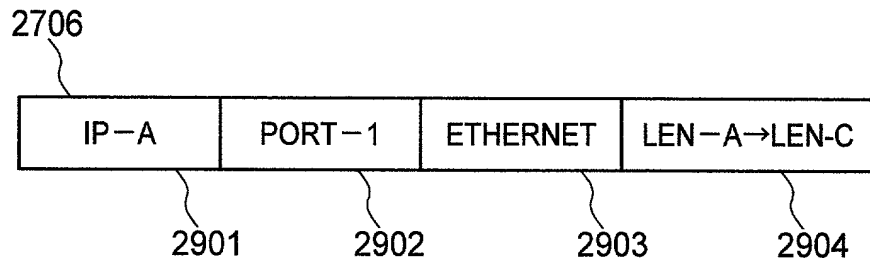


FIG. 29

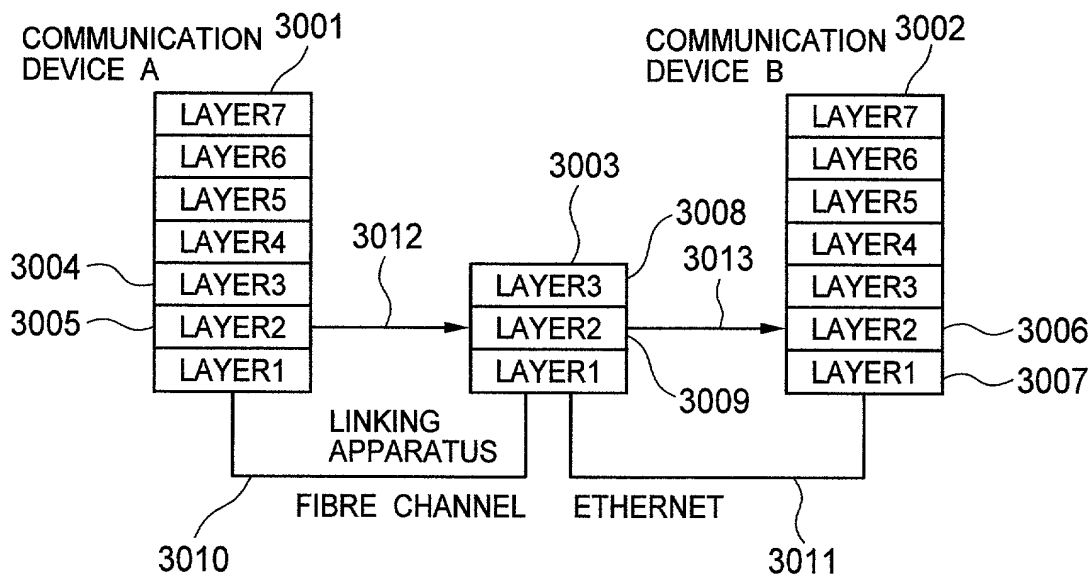


FIG. 30

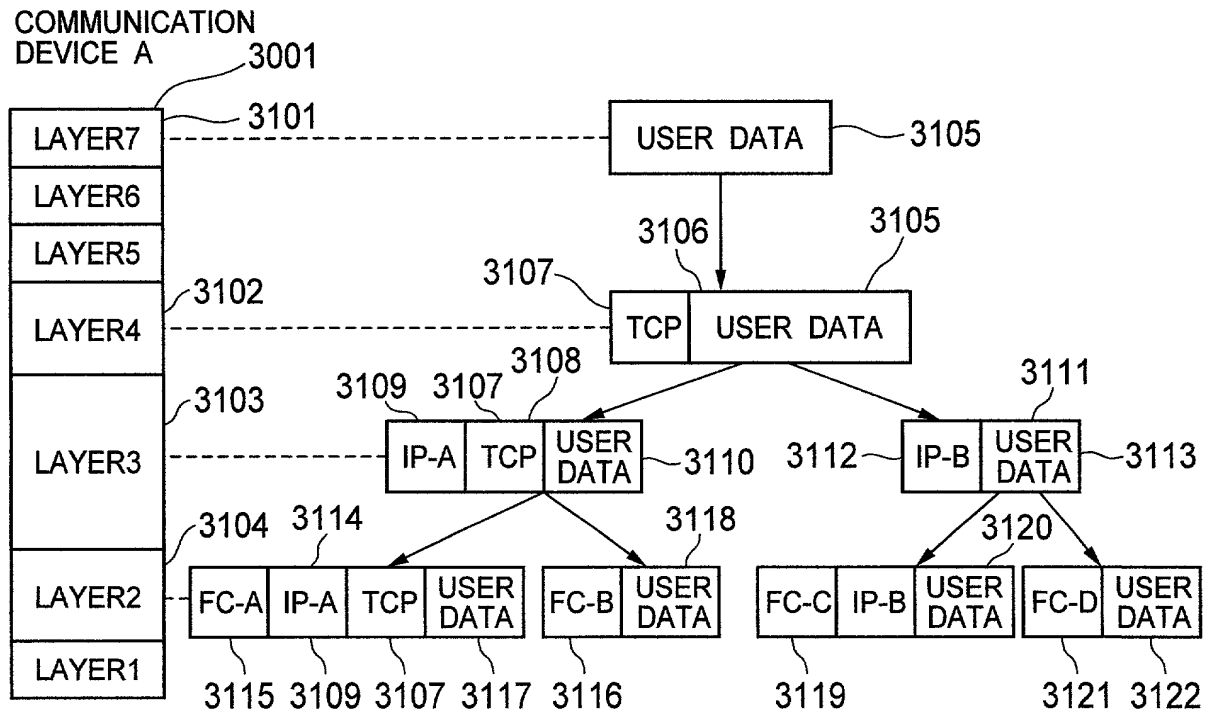


FIG. 31

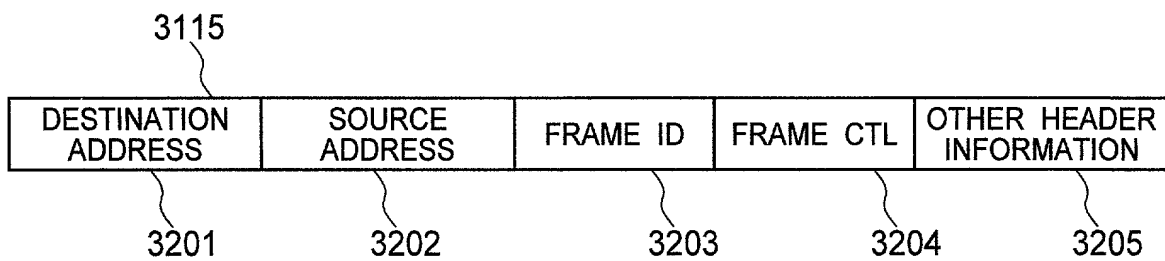


FIG. 32

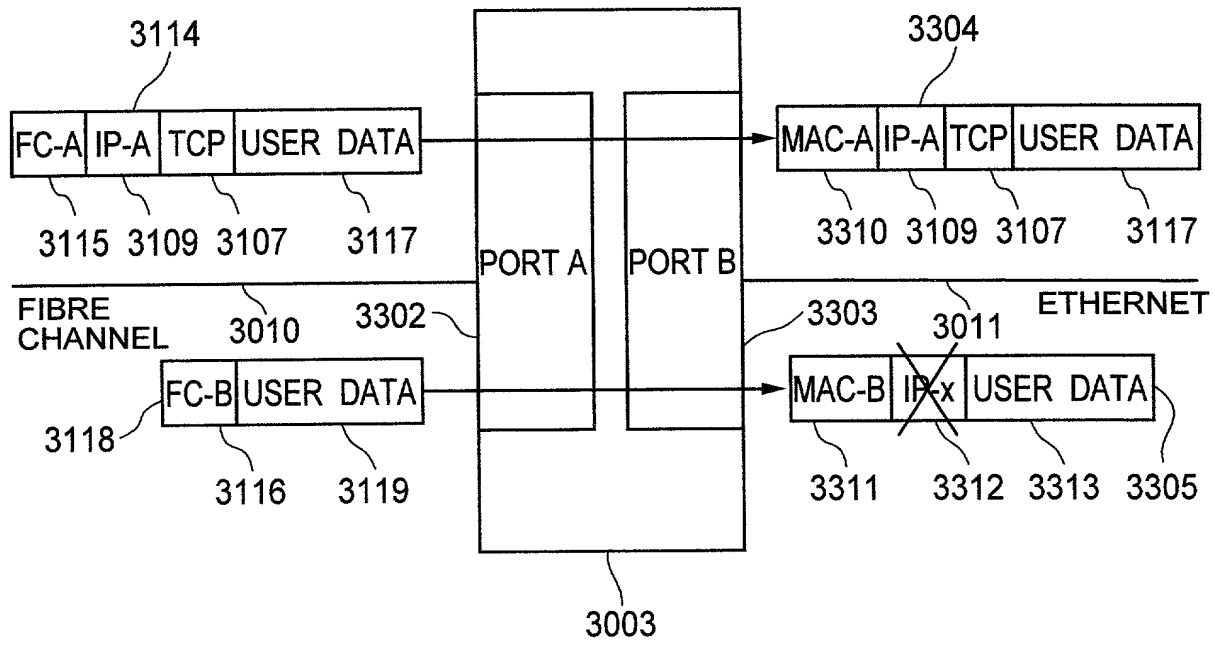


FIG. 33

| LAYER3 | LAYER2 | TRANSMISSION PORT | MAXIMUM PACKET LENGTH | 3401 |
|--------|--------|----------------------|--------------------------|------|
| IP-A | MAC-A | PORT B | LEN A | |
| 3109 | 3310 | 3303 | 3402 | |

FIG. 34

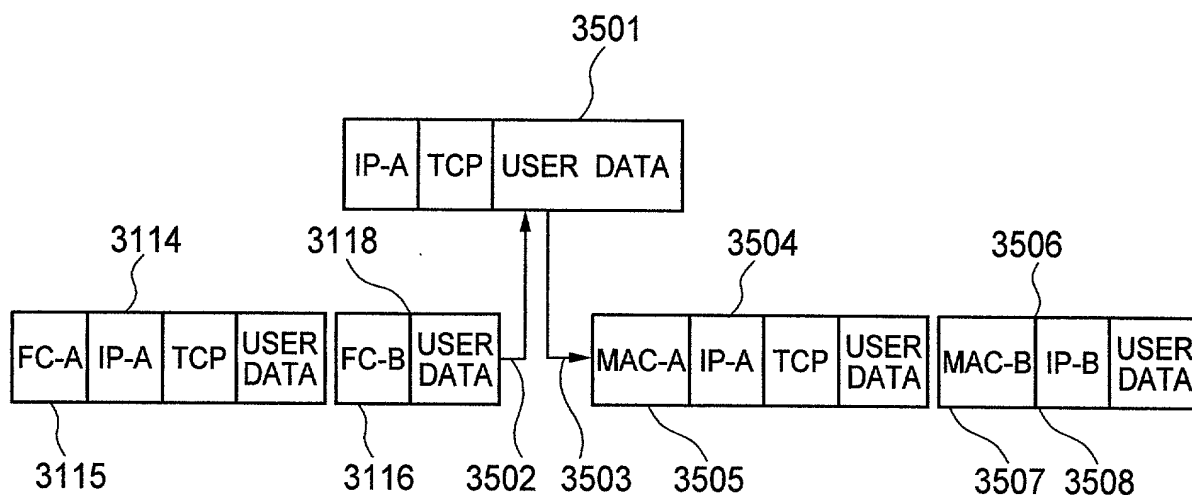


FIG. 35

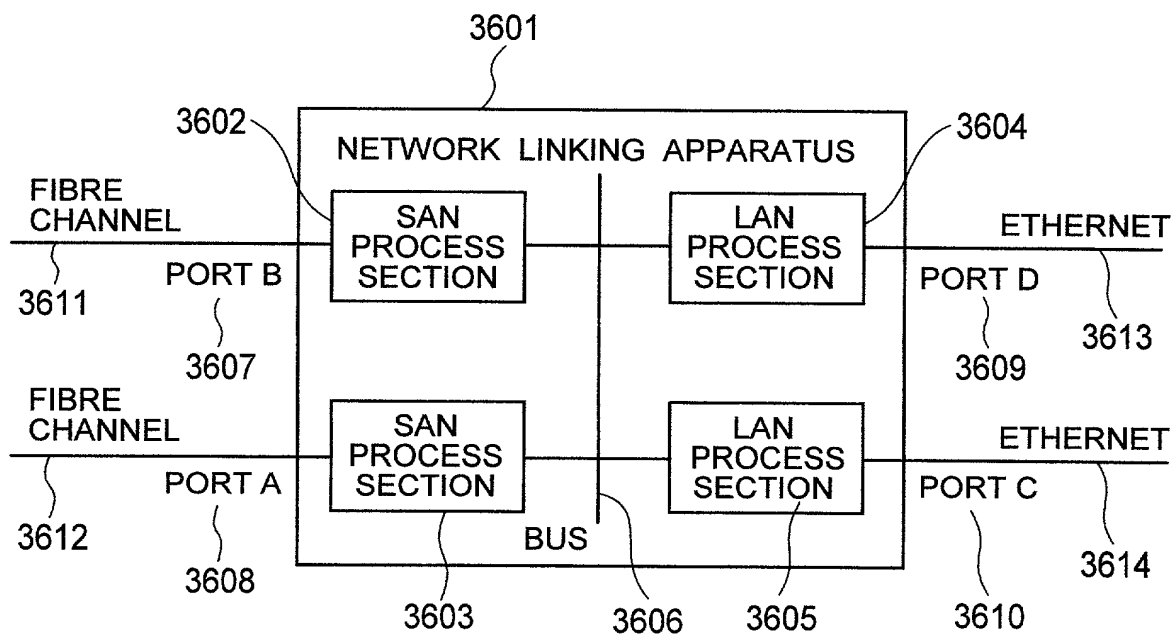


FIG. 36

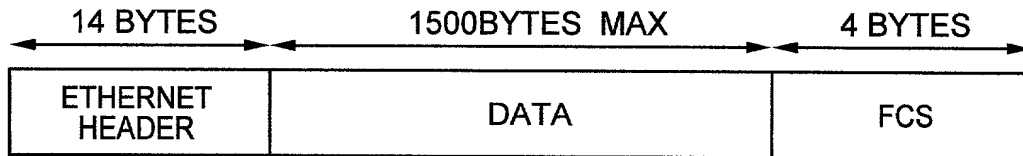


FIG. 37

